

Notice of References Cited	Application/Control No. 10/806,963		Applicant(s)/Patent Under Reexamination GOSS, STEVEN C.	
	Examiner WILLIAM S. POWERS		Art Unit 2134	Page 1 of 1

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*	B	US-7,340,614	03-2008	Fujiwara et al.	713/193
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NON-PATENT DOCUMENTS

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	U	Smith et al., "Using a High-Performance, Programmable Secure Coprocessor", 1998, Springer Berlin/Heidelberg, Vol. 1465/1998, pp. 73-89.
	V	Smith et al., "Building a High-Performance, Programmable Secure Coprocessor", 1998, Draft, IBM TJ Watson Research Center, pp. 1-39.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.